

**ABSTRACT OF THE DISCLOSURE**

A LED chip package including a base, an array of LED chips disposed on the base, and a collimator mounted on the base, over the array of light-emitting-diode chips. The LED chips of the array are typically arranged in an inline manner. The collimator is generally configured as a rectangular, horn-like member and typically includes a first set of walls that collimate the light emitted by the LED chips in a first direction and a second set of walls that minimally collimate the light emitted by the LED chips in a second direction.

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